

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re: Jong-Rong Jan et al;

Serial No. To be assigned

Filed: Concurrently herewith

For: **METHODS OF SELECTIVELY BUMPING INTEGRATED CIRCUIT
SUBSTRATES AND RELATED STRUCTURES**

Date: February 17, 2004

Mail Box PATENT APPLICATION

Commissioner for Patents

P.O. Box 1450

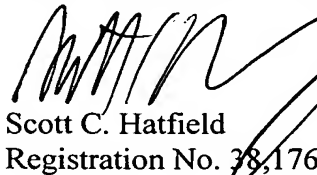
Alexandria, VA 22313-1450

APPLICATION FILED UNDER 37 CFR 1.41(c)

Sir:

The above-identified application is being filed on behalf of the inventors,
Jong-Rong Jan, Tsai-Hua Lu, Sao-Ling Chiu, and Ling-Chen Kung, all residents of
Taiwan, under the provisions of 37 CFR 1.41(c). A Declaration and Power of Attorney from
the inventors will follow, 37 CFR 1.63.

Respectfully submitted,



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